

## Altium / Bay Area Circuits Design Rule Comparison Chart

Comparison between Altium Designer's default design rules and the rules configured in BAC's Altium .RUL file.

	Altium Designer	Bay Area Circuits
<b>Electrical Clearance</b>		
Clearance	10 mil	5 mil
Short-Circuit	Disabled (Untick)	Disabled (Untick)
Un-Routed Net	Disabled	Enable
<b>Routing</b>		
Minimum Width	10 mil	5 mil
Preferred Width	10 mil	10 mil
Maximum Width	10 mil	150 mil
Routing Topology	Shortest	Shortest
<b>Routing Via Style (ML/DL)</b>		
Minimum Via Diameter	50 mil	20 mil
Maximum Via Diameter	50 mil	50 mil
Preferred Via Diameter	50 mil	30 mil
Minimum Via Hole Size	28 mil	10 mil
Maximum Via Hole Size	28 mil	15 mil
Preferred Via Hole Size	28 mil	15 mil
<b>Mask</b>		
Solder Mask Expansion	2 mil	5 mil
<b>Plane (Power Plane Connect Style)</b>		
Connect Style	Relief Connect	Relief Connect
Expansion	20 mil	15 mil
Air-Gap	10 mil	10 mil
Conductor Width	10 mil	10 mil
Conductors	4	4
<b>Power Plane Clearance</b>		
Plane Clearance	10 mil	5 mil
<b>Polygon Connect Style</b>		
Connect Style	Relief Connect	Relief Connect
Air Gap Width	10 mil	10 mil
Conductor Width	10 mil	10 mil
Rotation	90°	90°
Conductors	4	4
<b>Manufacturing</b>		
Minimum Annular Ring	10 mil	5 mil
Measurement Method	Absolute	Absolute
Minimum Hole Size (DL)	1 mil	10 mil
Minimum Hole Size (ML)	1 mil	10 mil
Maximum Hole Size (DL)	100 mil	247 mil
Maximum Hole Size (ML)	100 mil	247 mil